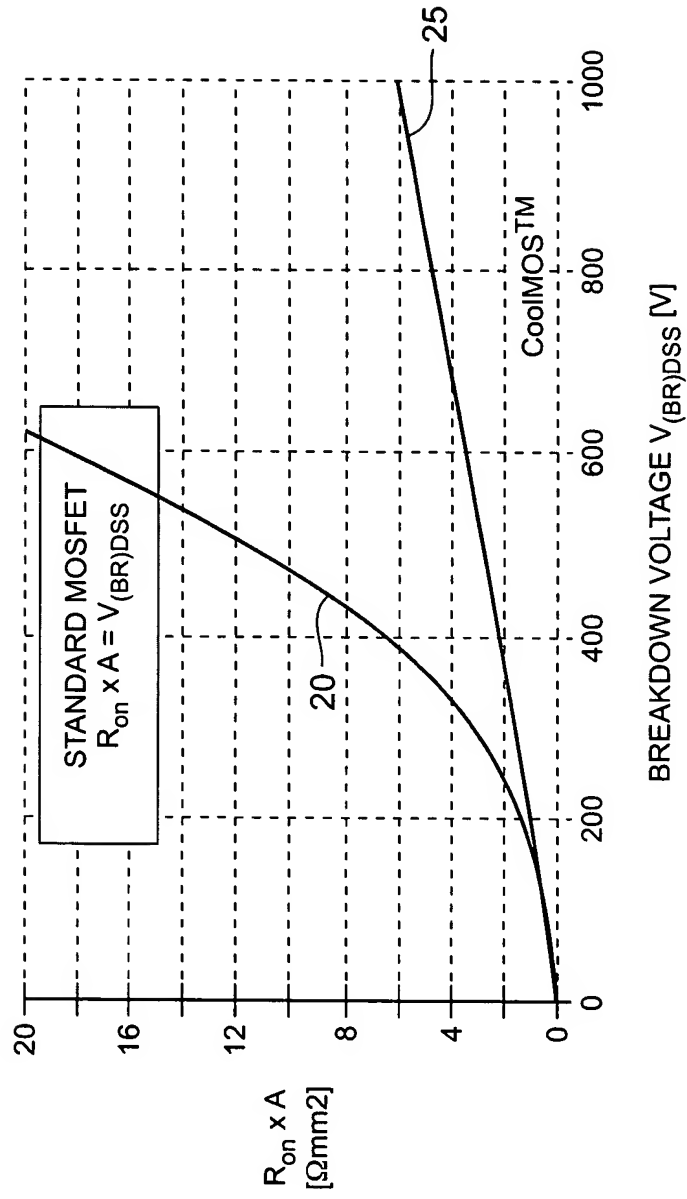


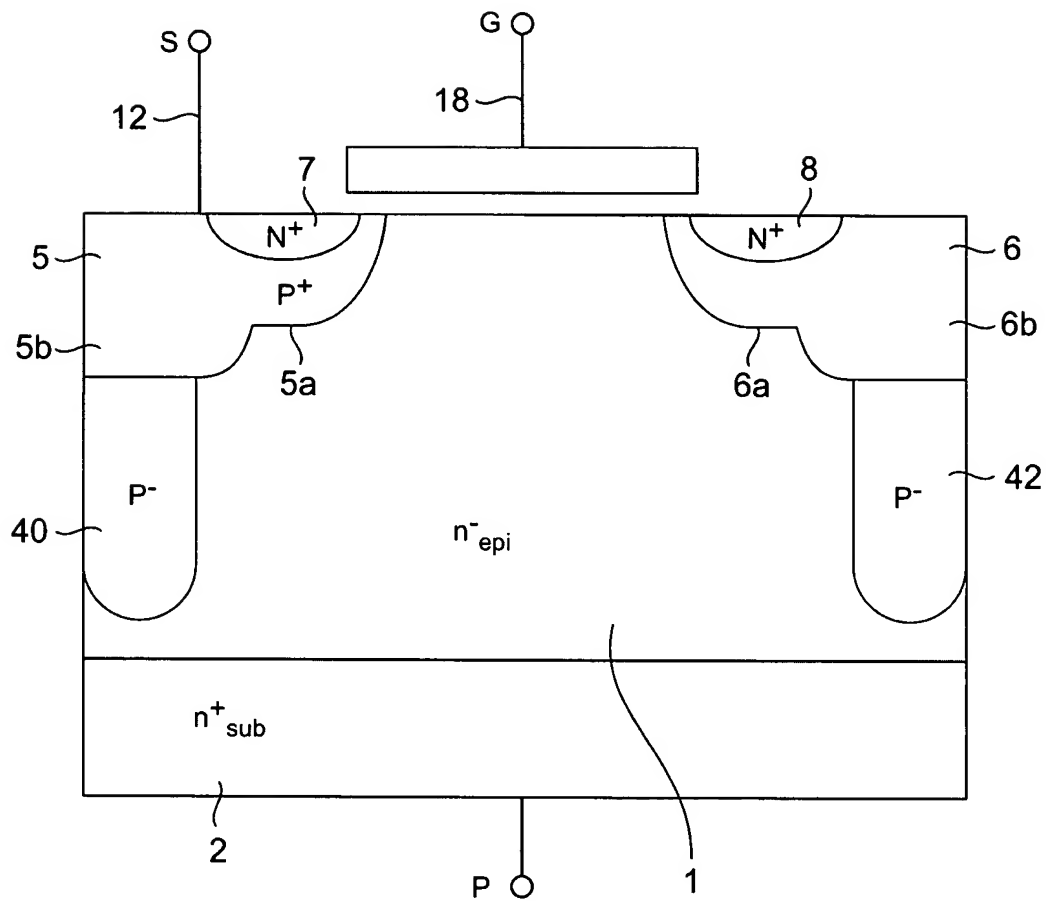
CONVENTIONAL MOSFET

FIG. 1
(PRIOR ART)



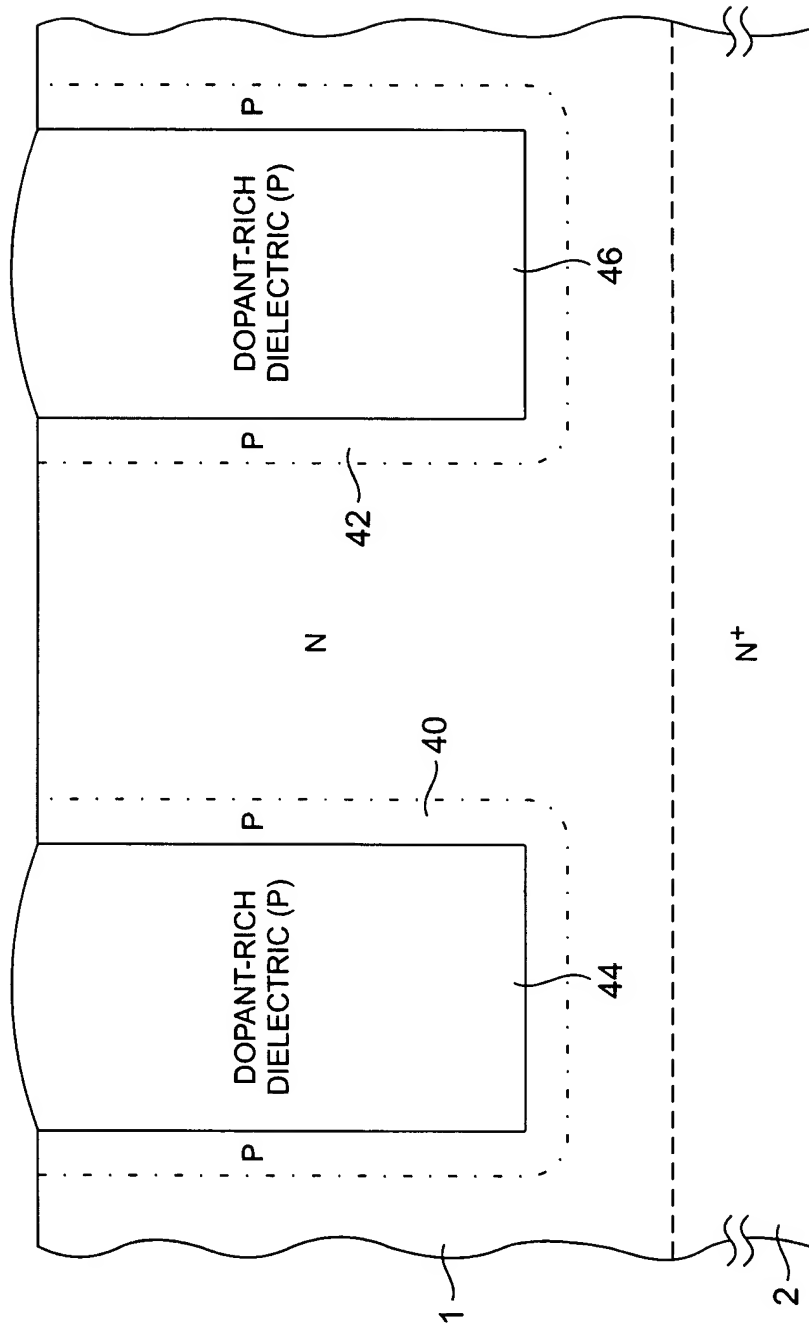
THE SPECIFIC ON-RESISTANCE OF A VERTICAL DMOS TRANSISTOR WITH THE DOPANT DISTRIBUTION OF FIG. 1

FIG. 2



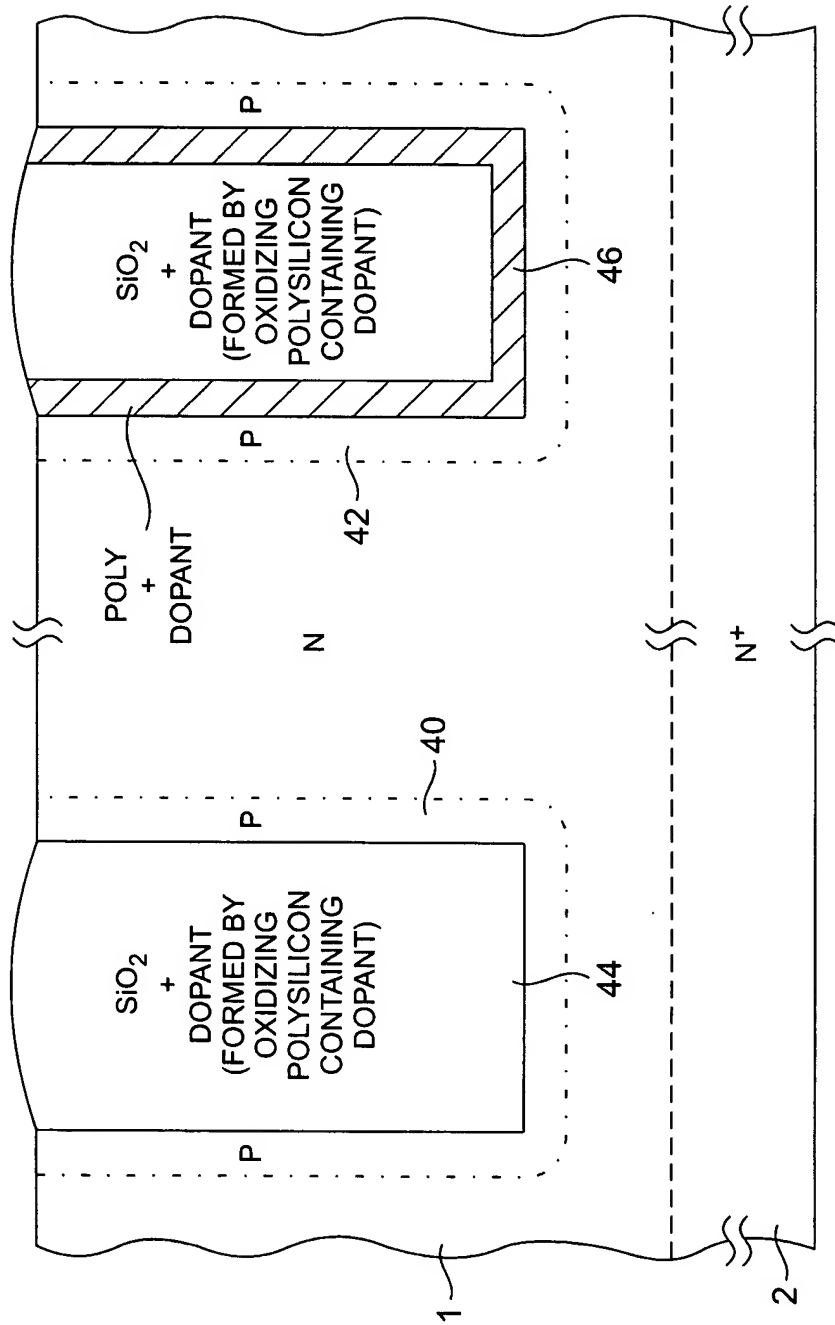
THE DOPANT DISTRIBUTION OF A
HIGH VOLTAGE VERTICAL DMOS TRANSISTOR
WITH A RELATIVELY LOW ON-RESISTANCE

FIG. 3
(PRIOR ART)



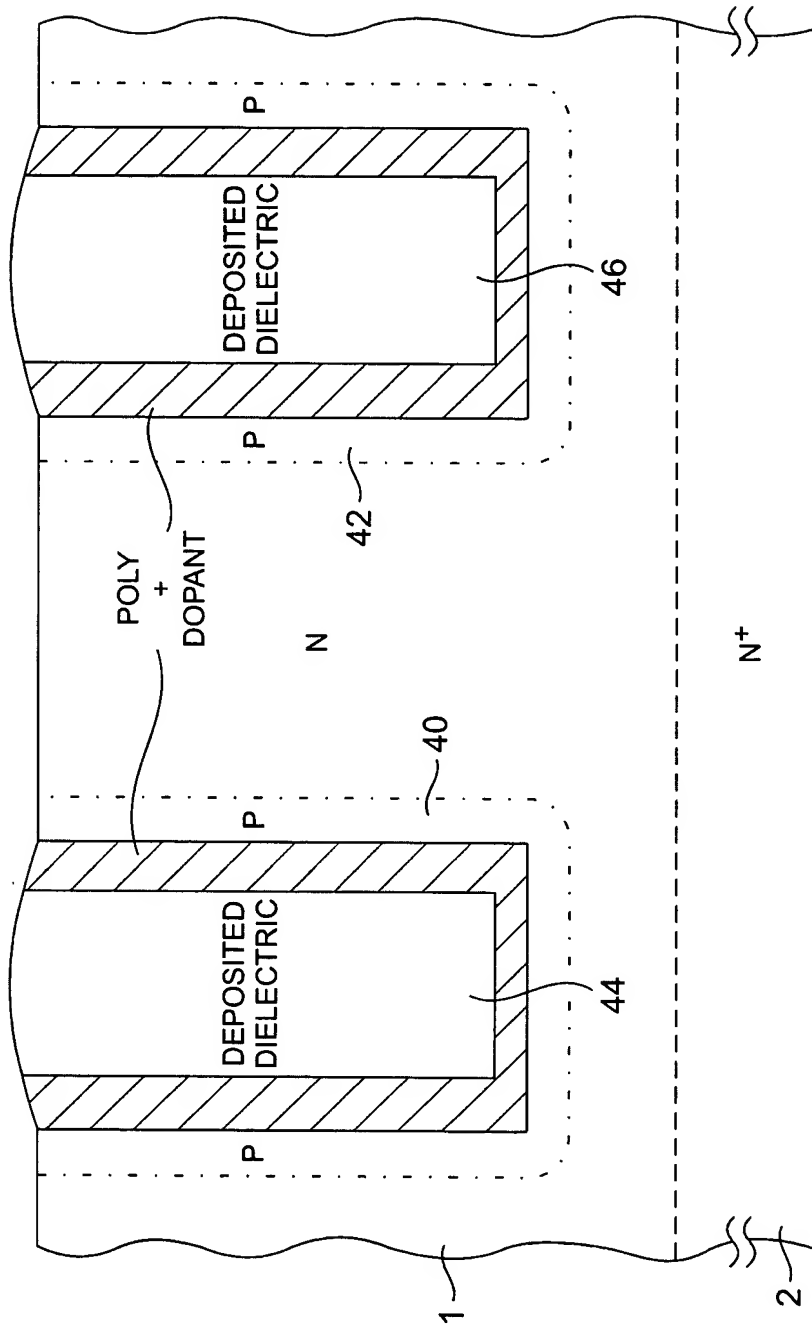
A DOPING PROFILE THAT WAS DIFFUSED FROM
TRENCHES FILLED WITH DOPANT-RICH DIELECTRIC

FIG. 4



A DOPING PROFILE THAT WAS DIFFUSED FROM
TRENCHES CONTAINING DOPED POLYCRYSTALLINE
SILICON THAT WAS SUBSEQUENTLY OXIDIZED

FIG. 5



A DOPING PROFILE THAT WAS DIFFUSED FROM A TRENCH CONTAINING DOPED POLYCRYSTALLINE SILICON AS WELL AS DEPOSITED DIELECTRIC

FIG. 6

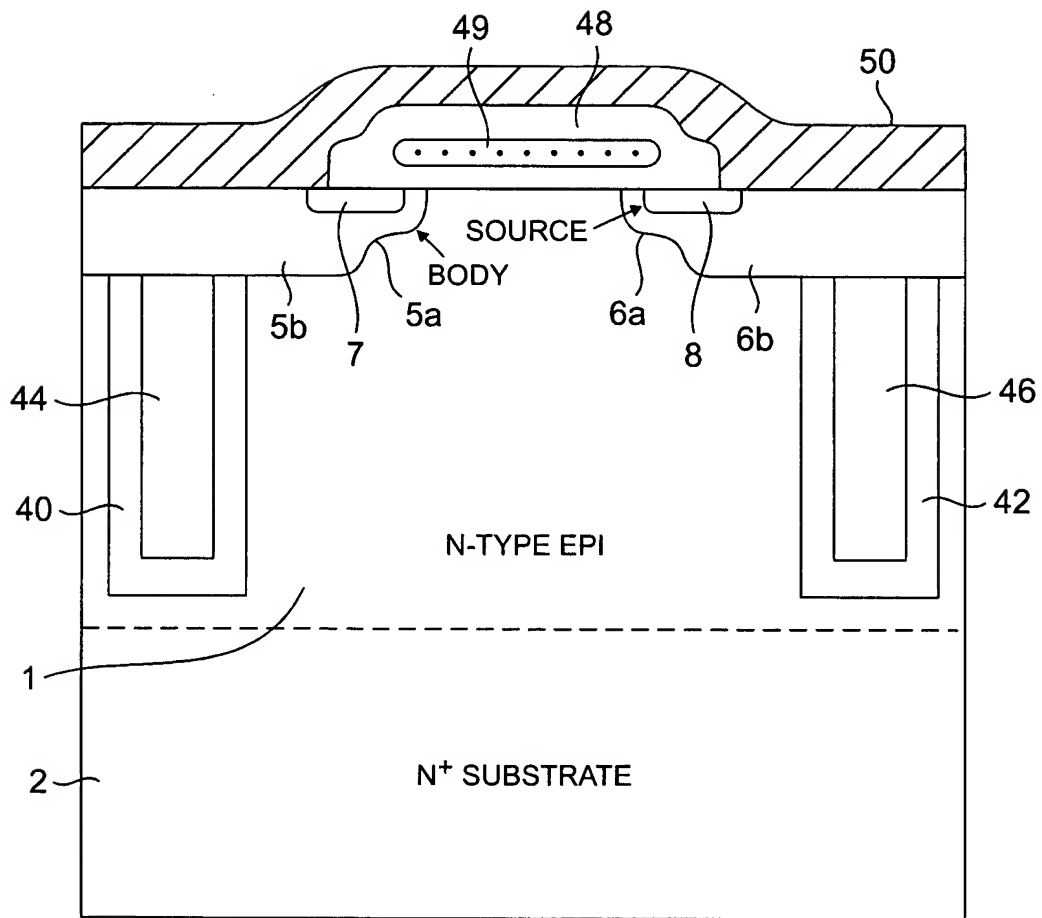


FIG. 7